

Some Developments for the R Engine

Luke Tierney

Department of Statistics & Actuarial Science
University of Iowa

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- R is a language for data analysis and graphics.
- R is based on the S language developed by John Chambers and others at Bell Labs.
- R is widely used in the field of statistics and beyond, especially in university environments.
- R has become the primary framework for developing and making available new statistical methodology.
- Many (over 3,000) extension packages are available through CRAN or similar repositories.



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- Developed by the R-core group since mid 1997,

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Luke Tierney	Simon Urbanek	
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 - contributed extension packages
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This talk outlines some recent developments in the core R engine I have worked on and developments I expect to work on over the next 12 to 18 months:

- Byte code compilation of R code.
- Taking advantage of multiple cores for
 - basic vectorized operations
 - simple matrix operations.
- Increasing the limit on the size of vector data objects.



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- The standard R evaluation mechanism
 - parses code into a *parse tree* when the code is read
 - evaluates code by interpreting the parse trees.
- Most low level languages (e.g. C, FORTRAN) compile their source code to native machine code.
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- Byte code is the machine code for a *virtual machine*.
- Virtual machine code can then be interpreted by a simpler, more efficient interpreter.
- Virtual machines, and their machine code, are usually specific to the languages they are designed to support.
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- The first release of the compiler occurred with R 2.13.0 this spring.
- Some improvements in the virtual machine interpreter were released with R 2.14.0 this fall.
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- The compiler can be called explicitly to compile single functions or files of code:
 - `cmpfun` compiles a function
 - `cmpfile` compiles a file to be loaded by `loadcmp`
- It is also possible to have package code compiled when a package is installed.
 - Use `--byte-compile` when installing, or specify the `ByteCompile` option in the `DESCRIPTION` file.
 - R 2.14.0 by default compiles R code in all base and recommended packages.
- Alternatively, the compiler can be used in a JIT mode where
 - functions are compiled on first use
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- The current compiler includes a number of optimizations, such as
 - constant folding
 - special instructions for most SPECIALs, many BUILTINS
 - inlining simple `.Internal` calls: e.g.

```
dnorm(y, 2, 3)
```

is replaced by

```
.Internal(dnorm(y, mean = 2, sd = 3, log = FALSE))
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 - special instructions for many `.Internals`
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A Simple Example

R Code

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f <- function(x) {  
  s <- 0.0  
  for (y in x)  
    s <- s + y  
  s  
}
```

VM Assembly Code

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LDCONST 0.0  
SETVAR s  
POP  
GETVAR x  
STARTFOR y L2  
L1: GETVAR s  
GETVAR y  
ADD  
SETVAR s  
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Some Performance Results

Timings for some simple benchmarks on an x86_64 Ubuntu laptop:

<i>Benchmark</i>	<i>Interp.</i>	<i>Comp.</i>	<i>Speedup</i>	<i>Exper.</i>	<i>Speedup</i>
p1	32.19	7.98	4.0	1.47	21.9
sum	6.72	1.86	3.6	0.59	11.4
conv	14.48	4.30	3.4	0.81	17.9
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Interp., *Comp.* are for the development version of R

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Exper.: experimental version using

- separate instructions for vector, matrix indexing
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- The current virtual machine uses a stack based design.
- An alternative approach might use a register-based design.
- Some additional optimizations currently being explored:
 - avoiding the allocation of intermediate values when possible
 - more efficient variable lookup mechanisms
 - more efficient function calls
 - possibly improved handling of lazy evaluation

Some promising preliminary results are available.

- Other possible directions include
 - Partial evaluation when some arguments are constants
 - Intra-procedural optimizations and inlining
 - Declarations (sealing, scalars, types, strictness)
 - Machine code generation using LLVM or other approaches
- Collaborations with computer scientists are being explored.
- Maintainability is a key concern.



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Parallelizing Vector and Matrix Operations

- Most modern computers feature two or more processor cores.
- It is expected that tens of cores will soon be common.
- A common question:

How can I make R use more than one core for my computation?

- There are many easy answers.
- But this is the wrong question.
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- Explicit parallelization:
 - uses some form of annotation to specify parallelism
 - packages `snow`, `multicore`, `parallel`.
- Implicit parallelization:
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I will focus on implicit parallelization of

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Parallelizing Vectorized Operations

An Idealized View

- Basic idea for computing $f(x[1:n])$ on a two-processor system:
 - Run two worker threads.
 - Place half the computation on each thread.
- Ideally this would produce a two-fold speed up.



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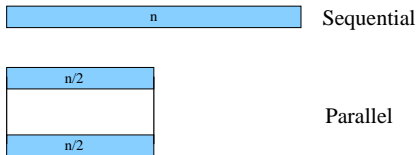
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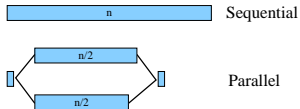




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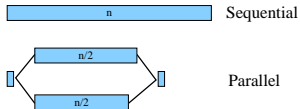
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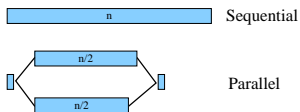
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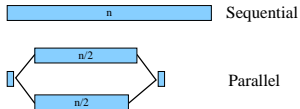
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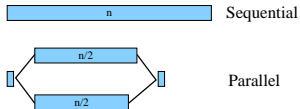
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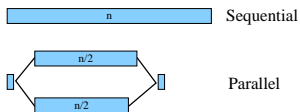
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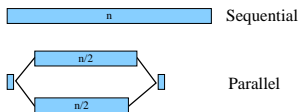
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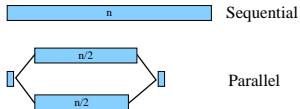
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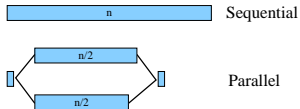
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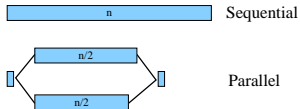
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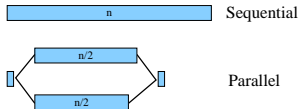
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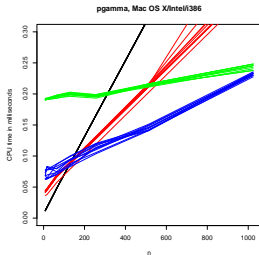
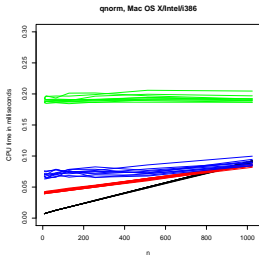
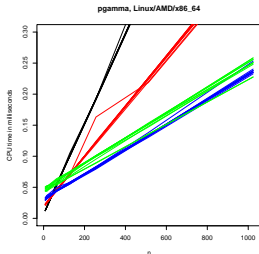
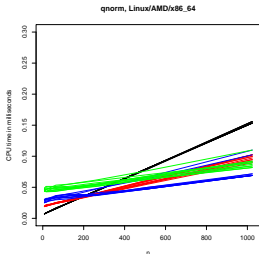


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Some Experimental Results





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Some observations:

- Times are roughly linear in vector length.
- Intercepts on a given platform are roughly the same for all functions.
- If the slope for P processors is s_P , then at least for $P = 2$ and $P = 4$,

$$s_P \approx s_1/P$$

- Relative slopes of functions seem roughly independent of OS/architecture.

A simple calibration strategy:

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The appropriate time to run calibration code is still open.



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- Relative slopes of functions seem roughly independent of OS/architecture.

A simple calibration strategy:

- Compute relative slopes once, or average across several setups.
- For each OS/architecture combination compute the intercepts.

The appropriate time to run calibration code is still open.



Parallelizing Vectorized Operations

Implementation

- Need to use threads
- One possibility: using raw pthreads
- Better choice: use Open MP
- Open MP consists of
 - compiler directives (#pragma statements in C)
 - a runtime support library
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  default(shared) private(i) reduction(&&:naflag)  
  for (i = 0; i < n; i++) {  
    double ai = a[i];  
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- Steps in converting to Open MP:

- check f is thread-safe; modify if not
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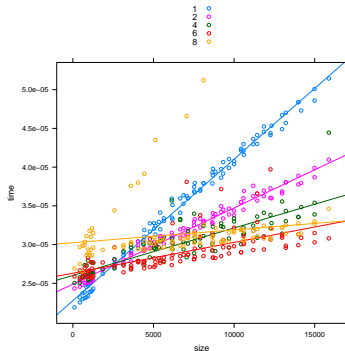
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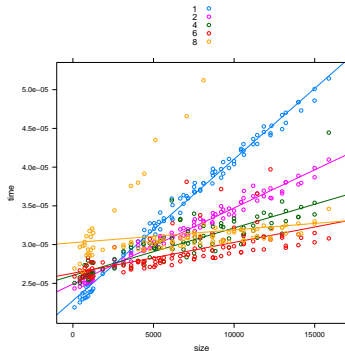


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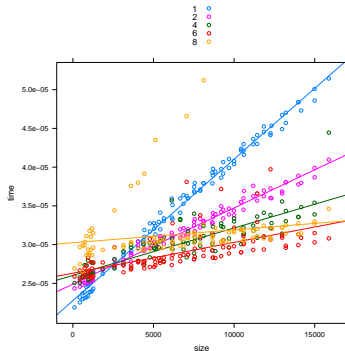


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Some issues to consider:

- Again using too many processor cores for small problems can slow the computation down.
- `colSums` can be parallelized by rows or columns:
 - Handling groups of columns in parallel produces identical results to a sequential version.
 - Handling groups of rows in parallel changes numerical results slightly (floating point addition is not associative).
- `rowSums` is slightly more complex since locality of reference (column major storage) need to be taken into account.
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Using a Parallel BLAS

- Most core linear algebra calculations use the Basic Linear Algebra Subroutines library (BLAS).
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- Both Intel and AMD provide sequential and threaded accelerated BLAS implementations.
- Atlas and Goto's BLAS also come in sequential and threaded versions.
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- This will allow gains from parallelizing compound operations on shorter vectors.



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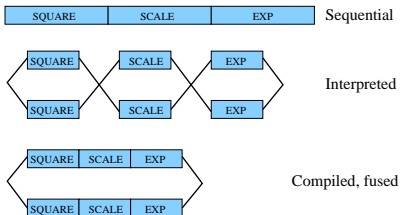
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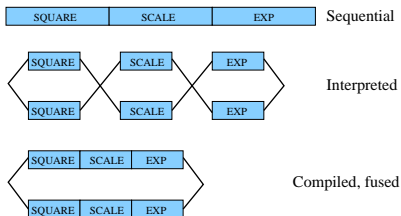


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- Currently The total number of elements in a vector cannot exceed $2^{31} - 1 = 2,147,483,647$
- This limit represents the largest possible 32-bit signed integer.
- For numeric (double precision) data this means the largest possible vector is about 16 GB.
- This is fairly large, but is becoming an issue with larger data sets with many variables on 64-bit platforms.
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- For all practical purposes on all current architectures the C `int` type and the FORTRAN `integer` type are 32 bit signed integers.
- The R source code uses C `int` or FORTRAN `integer` types in many places that would need to be changed to a wider type.
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Some Possible Directions

- A possible strategy:
 - Change length fields in internal headers to support longer vectors.
 - Change standard field accessors to signal an error if long vectors are used.
 - Add new accessors that allow long vectors.
 - Gradually introduce long vector support into the R internals.
- The initial header change will require recompiling all C code but no further code changes.
- After the header change, support for large vectors can be introduced incrementally in R itself and in packages.
- It may eventually be necessary to introduce a long integer data type or change the integer type from 32 to 64 bits.
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